

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	317	714/55.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:03
L12	255	714/731.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:03
L13	297	714/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:03
L14	235	714/814.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:03
L15	294	714/815.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:03
L16	583	714/819.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:04
L17	434	713/401.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:04
L18	781	713/500.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:04
L19	743	713/600.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:04

L21	222	714/744.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 13:05
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	19	three with logic with identical with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 11:08
L5	125	identical adj logic adj circuits	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 11:11
L6	5	(identical adj logic adj circuits) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 11:24
L7	819	memory with receiv\$3 with output\$1 with (logic adj circuit\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 11:24
L8	32	(memory with receiv\$3 with output\$1 with (logic adj circuit\$1)) and (identical with (logic adj circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 11:26
L9	5	(memory with receiv\$3 with output\$1 with (logic adj circuit\$1)) and (identical adj (logic adj circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 12:59
S33	14	transpar\$4 adj flip-flop	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 11:48
S34	3	(transpar\$4 adj flip-flop) same control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 10:39
S39	5027	((error with code) or parity) with (IC or chip or circuit) with output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:00

S40	51	((error with code) or parity) with (IC or chip or circuit) with output) and (714/746.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:02
S41	4	((error with code) or parity) with logic with (IC or chip or circuit) with output) and (714/746.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:04
S42	1767	(generat\$3 with ((error with code) or ECC or parity) with circuit with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:05
S43	19	(generat\$3 with ((error with code) or ECC or parity) with circuit with output) and (714/746.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:08
S44	7	(generat\$3 with ((error adj control adj code) or parity) with circuit with output) and (714/746.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 12:11
S45	1	(generat\$3 with ((error adj control adj code) or parity) with circuit with output) and (transient adj (disturbance\$1 or problem\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 15:16
S48	3225	delay\$3 with signal\$1 with clock with compar\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 16:24
S49	49	(delay\$3 with signal\$1 with clock with compar\$3) and (713/401.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 16:25
S50	16	(delay\$3 with signal\$1 with clock with compar\$3 with circuit\$1 with output\$1) and (713/401.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 16:28
S56	127	(delay\$3 with clock with (compar\$3 or analyz\$3) with output\$1 with flip-flop\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 16:35

S57	8	(delay\$3 with clock with (compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("714"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:08
S58	60	(delay\$3 with clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("714"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:31
S59	6	(clock adj delay\$3) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("714"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:10
S62	17	((clock adj delay\$3) same ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:11
S63	2	(delay\$3 with clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("714"/814.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:34
S64	1	(delay\$3 with clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("714"/815.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:34
S66	2	(delay\$3 with clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and (713/401.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:37
S67	24	(delay\$3 with clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("713"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 17:46
S68	8	(delay\$3 adj clock) and ((compar\$3 or analyz\$3) with output\$1 with flip-flop\$1) and ("713"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/30 10:48